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06EC61

Sixth Semester B.E. Degree Examination, June-July 2009
Digital Communication

Time: 3 hrs.

Max. Marks:100

Note:1. Answer any FIVE full questions, selecting at least TWO questions from each Part A and Part B.
2. Missing data may be suitably assumed.

PART - A

- 1 a. Obtain an expression for Fourier Transform of a sampled signal. Assume flat top sampling. (08 Marks)
 b. A bandpass signal $g(t)$ with a spectrum shown below figure Q1 (b) is ideally sampled. Sketch the spectrum of sampled signals at $f_s = 25$ and 45 Hz. Indicate if and how the signal can be recovered. (08 Marks)

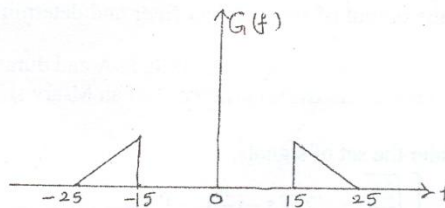


Fig. Q1 (b)

- c. What is 'aperture effect'? How is it eliminated? (04 Marks)
- 2 a. Explain the need for nonuniform quantization. Also explain μ -law and A-law companding. (08 Marks)
 b. If E denotes the energy of a strictly bandlimited signal $g(t)$, then prove that $E = \frac{1}{2\omega} \sum_{n=-\infty}^{\infty} \left| g\left(\frac{n}{2\omega}\right) \right|^2$, where ω is the highest frequency component of $g(t)$. (06 Marks)
 c. The signal $g(t) = 2 \cos(2000\pi t) - 4 \sin(4000\pi t)$ is quantized by rounding off, using a 12-bit quantizer. What is the rms quantization error and the quantization SNR? (06 Marks)
- 3 a. Consider a speech signal with a maximum frequency of 3.4 kHz and maximum amplitude of 1 volt. The speech signal is applied to a D.M. with its bit rate at 20 kbits/sec. Discuss the choice of an appropriate stepsize for the delta modulator. (05 Marks)
 b. Compare PCM and DPCM. (04 Marks)
 c. Obtain power spectral density of NRZ bipolar format and draw its normalized PSD. (11 Marks)
- 4 a. Design a binary baseband PAM system to transmit data at a bit rate of 3600 bits/sec, with a bit error probability $< 10^{-4}$. The channel response is given by, $H_c(f) = \begin{cases} 10^{-2} & |f| < 2400 \\ 0 & \text{elsewhere} \end{cases}$
 The noise power spectral density is $G_n(f) = 10^{-14}$ watts/Hz, $Q(y) \leq 10^{-4}$, $y \geq 3.75$, parameter $\beta = \frac{r_b}{6}$. (10 Marks)
 b. A binary data sequence is 011011. Sketch the waveform for the following formats:
 i) RZ unipolar ii) NRZ Bipolar (04 Marks)
 c. With a neat structure, explain the concept of the adaptive equalization process. (06 Marks)

PART - B

- 5 a. Explain with a neat block diagram the coherent QPSK Transmitter and Receiver. (08 Marks)
 b. A binary FSK system transmits data at a rate of 2MBPS over an AWGN channel. The noise is zero mean with PSD, $\frac{N_0}{2} = 10^{-20}$ W/Hz . The amplitude of received signal in the absence of noise is $1 \mu\text{V}$. Determine the average probability of error for coherent detection of FSK. Take $\text{erfc}\sqrt{6.25} = 0.00041$ (06 Marks)
 c. Show that the energy of signal $S_i(t)$ is equal to the square of length of the corresponding vector S_i . (06 Marks)
- 6 a. What do you mean by an optimum receiver with reference to a digital modulation scheme? Write the scheme of a correlation receiver and describe its features. (06 Marks)
 b. Find the output of the matched filter and determine the maximum value of $\frac{S}{N_0}$ if the input $s(t)$ is a rectangular pulse of amplitude A and duration T. (08 Marks)
 c. Calculate the bandwidth efficiency of an M-ary signaling scheme. (06 Marks)
- 7 a. Consider the set of signals,

$$S_i(t) = \begin{cases} \sqrt{\frac{2E}{T}} \cos(2\pi f_c t - i\frac{\pi}{4}) & 0 \leq t \leq T \\ 0 & \text{elsewhere} \end{cases}$$
 where $i = 0, 1, 2, 3$ and f_c is an integer multiple of $\frac{1}{T}$.
 i) Determine the dimensionality N of the signal set. (10 Marks)
 ii) Determine a set of orthogonality N of the signal set.
 iii) Determine the coefficients S_{ij} of the signals $S_i(t)$.
 iv) Give the signal constellation diagram.
 b. What is spread spectrum communication? What is its primary advantage? What are the commonly used spread spectrum technique? (07 Marks)
 c. Write the applications of spread spectrum technique. (03 Marks)
- 8 Write short notes on:
 a. Duobinary signaling.
 b. Eye pattern.
 c. Correlation receiver.
 d. TDM. (20 Marks)

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06EC62

Sixth Semester B.E. Degree Examination, June-July 2009

Microprocessor

Time: 3 hrs.

Max. Marks:100

- Note: 1. Answer any FIVE full questions, selecting at least TWO questions from each part.
2. Standard notations are used.
3. Missing data be suitably assumed.**

PART – A

- 1 a. Draw the internal architecture of the 8086 and explain. Briefly explain the flag register. (10 Marks)
- b. Explain the transient program area (TPA) and system area of a personal computer. (10 Marks)
- 2 a. Write an ALP to multiply two 32 bit numbers stored in consecutive memory locations and store the result in the memory. (10 Marks)
- b. Differentiate between Direct program memory addressing, Relative program memory addressing and Indirect program memory addressing with one or two examples. (06 Marks)
- c. If DS = AB30H, CS = 8920H, SS = 2B01H, BP = 2D45H, SP = 0130H, SI = 1234H DI = 4356H then determine the physical address of the following instructions. (04 Marks)
 - i) MOV [BP + DI + 5], AH
 - ii) MOV AL, [5036H]
- 3 a. Explain the following string instructions: (10 Marks)
 - i) MOVS
 - ii) Repeat Prefix (REP)
 - iii) STOSW
 - iv) SCASB
 - v) CMPS.
- b. Write an ALP to convert lowercase to uppercase using modular programming approach. Use two far procedures one for reading from keyboard and one for displaying. (10 Marks)
- 4 a. Explain the following DOS system call : int 21H functions: (10 Marks)
 - i) INT 21H, Function 001H
 - ii) INT 21H, Function 08H
 - iii) INT 21H, Function 0AH
 - iv) INT 21H, Function 2BH
 - v) INT 21H, Function 2DH
- b. Draw the pin-out of the 8259 A programmable interrupt controller (PIC) and describe each pin. (10 Marks)

PART - B

- 5 a. Explain Isolated and Memory – Mapped I/O. (05 Marks)
- b. Explain about the following I/O instructions (05 Marks)
 - i) IN with fixed address
 - ii) IN with variable address
 - iii) INSB
 - iv) OUT with fixed address
 - v) OUTSW
- c. Explain the programmable peripheral interface (PPI) with command bytes of the command register in the 82C55. (10 Marks)
- 6 a. Draw the internal structure of 80 x 87 arithmetic coprocessor and explain. (10 Marks)
- b. Explain the following 8087 coprocessor instructions: (10 Marks)
 - i) FSQRT;
 - ii) FSTP;
 - iii) F SCALE;
 - iv) F RNDINT;
 - v) F COM
- 7 a. Write short notes on the following: (10 Marks)
 - i) Peripheral Component Interconnect (PCI);
 - ii) Parallel printer interface (LPT).
- b. Explain the Universal Serial Bus (USB) with PIN configuration, USB Data, USB commands. (10 Marks)
- 8 a. Write a note on Pentium microprocessor. (10 Marks)
- b. Explain about special 80386 registers. (10 Marks)

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06EC63

Sixth Semester B.E. Degree Examination, June-July 2009
Analog and Mixed Mode VLSI Design

Time: 3 hrs.

Max. Marks:100

**Note: Answer any FIVE full questions, selecting
at least TWO questions from each part.**

PART - A

- 1 a. State the reasons for the pedestal error, droop aperture error and sampling error. (08 Marks)
b. State and explain specifications of ADC. (12 Marks)
- 2 a. An 8 bit resistor string DAC was fabricated with a nominal resistor value of 1 k Ω . If the process was able to provide matching of resistors to within 1%, find maximum INL and DNL of the converter. Assume $V_{REF} = 5V$. (06 Marks)
b. Explain generic (unweighted) current steering DAC and discuss the related mismatch errors. (08 Marks)
c. Design a 4 bit charge scaling DAC using a split array. Assume that $V_{REF} = 5V$ and that $C = 0.5$ pF. Draw the equivalent circuit for $D = 0001$ and 0010 and determine the value of the output voltage. (06 Marks)
- 3 a. Explain the principle of single slope ADC and the problems associated with it. (10 Marks)
b. Draw the block diagram for 4 bit successive approximation ADC with $V_{REF} = 5V$. Explain the same. Trace the output at various stages for $V_{in} = 3.7V$. (10 Marks)
- 4 a. Explain the purpose of each stage of a voltage comparator. Also explain the working of 1st stage. (10 Marks)
b. Show that multiplying quad acts as multiplier when all the MOSFETs in the multiplying quad have the same threshold voltage. (10 Marks)

PART - B

- 5 a. Determine the ideal SNR of a 8 bit data converter with averaging of 20 outputs. (04 Marks)
b. Draw the circuit arrangement used for decimation and averaging and explain the same. Determine the transfer function of the same. (10 Marks)
c. Bring out the principle of interpolation. (06 Marks)
- 6 a. Describe CMOS process flow with neat sketches. (10 Marks)
b. Explain how MOSFET behaves as a capacitor. Also explain floating MOS capacitor. (10 Marks)
- 7 a. Estimate the high-to-low and low-to-high delays in the circuits shown in figure Q7 (a). (08 Marks)

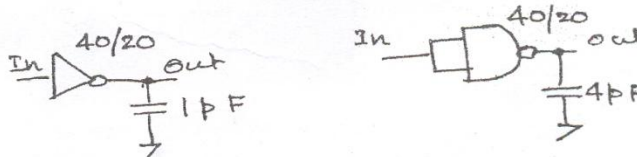


Fig. Q7 (a)

- b. Draw the arrangement for 4 bit pipelined adder and full adder bit implemented using dynamic logic. (06 Marks)
- c. Explain the working of simple delay element using pass transistor and CMOS inverter. (06 Marks)

- 8 a. Explain the limitations of inverter at the output of OPAMP, with the help of its transfer curve. How is it overcome? (07 Marks)
- b. Consider the AC small signal simplification of floating current source as in figure Q8 (b). Assuming NMOS cascade output resistance is labeled R_{NCOS} , what is the small signal resistance as seen by the test voltage V_{test} ? (07 Marks)

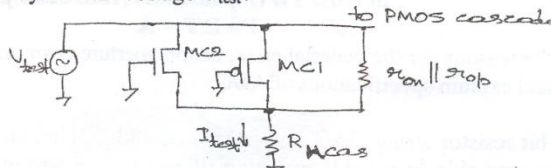


Fig. Q8 (b)

- c. Determine time constant of OPAMP with unity gain frequency of 100 MHz. Assume that all the outputs is fed back to the input. Also determine the settling time for 0.1% settling accuracy. (06 Marks)

- b. Explain mutual information. Find an expression for mutual information in terms of joint probability, probabilities of input and output symbols. (05 Marks)
- c. Show that mutual information is always positive. (05 Marks)
- 4 a. State and prove Shannon-Hartley law. Derive an expression for the upper limit on channel capacity as bandwidth tends to ∞ . (08 Marks)
- b. Two independent random variables x and y have density functions $f(x)$ and $f(y)$ as shown in Figure Q4 (b).



Fig. Q4 (b)

- i) Find the entropy of each signal and the joint entropy.
- ii) If the signals are overlapped find $f(x, y)$ and the joint entropy. (04 Marks)
- c. The output of a DMS consist of letters x_1, x_2, x_3 with probabilities 0.45, 0.35, 0.20 respectively.
- i) Compute the Huffman code for this source and also find code efficiency and variance.
- ii) If pairs of letter are encoded, compute the Huffman code, code efficiency and variance. (08 Marks)

PART - B

- 5 a. Explain the need and meaning of error control coding. (05 Marks)
- b. For a linear block code with generator matrix G and parity check matrix H , prove that $GH^T = 0$ in systematic format. (05 Marks)
- c. For a systematic (6, 3) linear block code the parity matrix, $[P] = \begin{bmatrix} 1 & 0 & 1 \\ 0 & 1 & 1 \\ 1 & 1 & 0 \end{bmatrix}$
- i) Find all possible code vectors.
- ii) Find the minimum weight of the code.
- iii) Find the parity check matrix.
- iv) For a received code vector $R = 1\ 1\ 1\ 1\ 0\ 1$ detect and correct the error that has occurred due to noise. (10 Marks)
- 6 a. Define cyclic code. Explain how cyclic codes are generated from the generating polynomials. (06 Marks)
- b. The generator polynomial for a (7, 4) binary cyclic code is $g(x) = 1 + x + x^3$
- i) Find the code vector in systematic form for a message vector 1 1 0 0.
- ii) Design an encoder for the above and verify its operation for message vector 1 1 0 0. (14 Marks)
- 7 Consider a (3, 1, 2) convolutional code with $g^{(1)} = 110, g^{(2)} = 101, g^{(3)} = 111$.
- a. Draw the encoder block diagram.
- b. Find the generator matrix.
- c. Find the code word corresponding to the information sequence (1 1 1 0 0) using time domain and transform domain approach.
- d. Draw the state table.
- e. Draw the state diagram.
- f. Draw the code tree and find encoder output for message sequence (1 1 1 0 0) (20 Marks)
- 8 Write short notes on:
- a. RS codes. b. Golay codes. c. Shortened cyclic codes. d. Burst error correcting codes. (20 Marks)

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06EC661

Sixth Semester B.E. Degree Examination, June-July 2009
Programming in C++

Time: 3 hrs.

Max. Marks:100

**Note: Answer any FIVE full questions, selecting
at least TWO questions from each part.**

PART - A

- 1 a. What is preprocessor directives? What are the different types of preprocessor directives used in C++? Explain each with an example. (10 Marks)
- b. What is dynamic memory allocation? What are the primary differences between static and dynamic memory allocation? (06 Marks)
- c. Explain the difference between the four objects defined below:
 - i) `int ival = 1024;` ii) `int *pi2 = new int(1024);`
 - iii) `int *pi = &ival;` iv) `int *pi3 = new int[1024];` (04 Marks)
- 2 a. Distinguish between lvalue and rvalue. Provide examples of both. (05 Marks)
- b. What are the sorts of operations that we expect from a string class? Write a program in C++ to find the length of the string using the string type. (10 Marks)
- c. Explain enumeration type with example. (05 Marks)
- 3 a. Discuss the following with example:
 - i) Increment and decrement operators
 - ii) The Bitwise operators. (08 Marks)
- b. Explain with the syntax, the components of the switch statement. Write a program in C++ to count the number of vowels in a given string. (12 Marks)
- 4 a. What is function prototype? With example explain the different types of argument passing mechanisms. (10 Marks)
- b. Write a recursive function to find the nth Fibonacci number. (05 Marks)
- c. Write an inline function to find the largest of two integers. (05 Marks)

PART - B

- 5 a. What is an exception? With the help of example, explain how the try block and catch block works. (10 Marks)
- b. Explain the following :
 - i) Exception specification
 - ii) Exceptions and design issues. (10 Marks)
- 6 a. Define class and object. How the data members are declared and accessed? Give example. (10 Marks)
- b. What is a constructor? Explain different types of constructors with examples. (10 Marks)
- 7 a. What is operator overloading? Write a program in C++ to concatenate two strings by overloading operator +.

`S1 = VTU S2 = BELGAUM S3 = S1 + S2` (10 Marks)
- b. Explain the following with example.
 - i) Overloaded operators ++ and --
 - ii) Overloaded operators new and delete. (10 Marks)
- 8 a. What is multiple inheritance? Explain with example. (10 Marks)
- b. Briefly explain public, private and protected inheritance. (06 Marks)
- c. Briefly explain the different forms of object composition. (04 Marks)

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06EC667

Sixth Semester B.E. Degree Examination, June-July 2009
Digital System Design Using VHDL

Time: 3 hrs.

Max. Marks:100

Note: 1. Answer any FIVE full questions, selecting at least TWO questions from each part.
2. Standard notations are used.

PART - A

- 1 a. Differentiate between :
 i) Signal and variable assignments (10 Marks)
 ii) Event and transaction. With suitable examples. (10 Marks)
- b. Write a VHDL program for the function.
 $Y = AB + C\bar{D}$ using i) Behavioral ii) Structural modeling. (10 Marks)
- 2 a. Draw the output macro cell of a PLD and explain its operation. (08 Marks)
 b. Implement the state table using RDM and DFF write a VHDL code for the same.

PS		NS		Output Z	
		Q ₁ *	Q ₂ *		
Q ₁	Q ₂	x = 0	x = 1	x = 0	x = 1
0	0	01	10	0	1
0	1	10	00	1	1
1	0	00	01	1	0

(12 Marks)

- 3 a. Find the reduced PLA table to realize the following functions.
 $f_1(a, b, c, d) = \sum m(4, 5, 10, 11, 12)$
 $f_2(a, b, c, d) = \sum m(0, 1, 3, 4, 8, 11)$
 $f_3(a, b, c, d) = \sum m(0, 4, 10, 11, 14)$ (06 Marks)
- b. Design a 3 digit BCD- binary converter draw the block diagram, state diagram and VHDL code. (10 Marks)
- c. Design a 2 bit \times 2 bit multiplier using address and gates. (04 Marks)
- 4 a. Draw the block diagram, state graph and VHDL code for a divider (8 bit dividend and 3 bit divisor) (12 Marks)
 b. Design a network that finds the square root of an 8 bit unsigned binary number. Explain the network. (08 Marks)

PART - B

- 5 a. An 8 bit right shift register with parallel load is to be implemented using xilinx 3000 logic cells. The FFs are labeled x₇ to x₀. The control signals N and S operate as follows.
 N = 0 no change
 NS = 10 load
 Ns = 11 right shift.
 How many logic cells are required? Show the required minimum connections. Give the equation of the last cell. (08 Marks)
- b. Show how to realize the following combinational functions using 2 xilinx 3000 logic cells.
 $F = \overline{x_1} x_2 \overline{x_3} x_6 + x_2 \overline{x_3} x_4 x_6 + x_2 x_3 \overline{x_4} x_6 + x_2 x_3 x_4 x_6 + \overline{x_3} x_4 x_5 x_6 + x_7$ (06 Marks)
- c. Explain with a neat sketch xilinx 3000 layout of a part of a programmable logic cell array. (06 Marks)

- 6 a. Give the contents of register A and B of a binary signed multiplier if $A = -\frac{1}{8}$ and $B = -\frac{3}{8}$. (06 Marks)
- b. Draw the block diagram for a floating point subtractor. Inputs are normalized. Fractions are 8 bits including sign bits and exponents are 5 bits including sign bit. Draw the SM chart. (10 Marks)
- c. If $A = 110101$ $B = 110010$, find $y = A \ll 2$ OR $B \ll 3$. (04 Marks)
- 7 a. Explain transport and inertial delays with example. (06 Marks)
- b. Explain signal attributes with examples. (06 Marks)
- c. Write a VHDL code to find dot product $\sum a_i * b_i$ for two integer vectors. (08 Marks)
- 8 a. Write a VHDL code for a static Ram with truth table

\overline{CS}	\overline{OE}	\overline{WE}	Mode	I/O pins
M	X	X	Not selected	High Z
L	H	H	Output disabled	High Z
L	L	H	Read	Data out
L	X	L	Write	Data in

- b. Write a VHDL code and synthesize the circuit for case statement. (08 Marks)
- c. Write a VHDL code to generate 3 input AND gate and 2 input AND gate with different delays using generic statement. (07 Marks)
- (05 Marks)

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Sixth Semester B.E. Degree Examination, June-July 2009
Satellite Communication

Time: 3 hrs.

Max. Marks:100

- Note:1. Answer any FIVE full questions, selecting at least TWO questions from each part.**
2. Missing data be suitably assumed.

PART - A

- 1 a. What is Diffraction? Derive an expression for knife-edge diffraction gain in dB. (08 Marks)
- b. Briefly explain the four phenomena of radio wave propagation. (06 Marks)
- c. A free space LOS microwave link operating at 10 GHz consists of a transmit and receive antenna with $G_t = G_r = 25$ dB. The distance between two antenna's is 30 KM and power radiated is 10W. Calculate the path loss and received power. (06 Marks)
- 2 a. Derive an expression for the relative dielectric constant of the Ionosphere in terms of N and f. (08 Marks)
- b. Briefly discuss about the Tropospheric Scatter. (06 Marks)
- c. Explain with the help of suitable diagram the skip distance. (06 Marks)
- 3 a. Explain briefly the development and functions of INTELSAT. (06 Marks)
- b. With suitable diagram explain various definitions of terms used to describe the position of the orbit with respect to the Earth. (10 Marks)
- c. A satellite is orbiting in two equatorial plane with a period from perigee to perigee of 12h. If the eccentricity is 0.002, calculate semi major axis. Assume the earth's equatorial radius as 6378.1414 km, $K_1 = 66063.1704 \text{ km}^2$ and $\mu = 3.986005 \times 10^{14} \text{ m}^3/\text{s}^2$ (04 Marks)
- 4 a. Explain what is meant by the earth eclipse of an earth - orbiting satellite, with suitable diagram? (08 Marks)
- b. What is meant by Geostationary Orbit? Why there is only one geostationary orbit? Explain. (08 Marks)
- c. Determine the angle of tilt required for a polar mount used with an earth station at a latitude 49° North. Assume spherical earth of mean radius 6371 km and ignore earth station altitude. $b = \lambda E$, $a_{G_{so}} = 42614 \text{ km}$ (04 Marks)

PART - B

- 5 a. Explain what is meant by EIRP? A transmitter feeds a power of 10W into an antenna which has a gain of 46 dB. Calculate the EIRP in dBW. (08 Marks)
- b. Explain rain rate and effective path length in connection with rain attenuation. (08 Marks)
- c. A satellite link operating at 14 GHz has a receiver feeder loss of 1.5 dB and free space loss of 207 dB. The atmospheric absorption loss is 0.5dB and antenna pointing loss is 0.5dB. Depolarisation losses are neglected. Calculate the total link loss for clear sky conditions. (04 Marks)
- 6 a. Describe TT and C facilities of a satellite communication system with the help of a neat block diagram. (08 Marks)
- b. Explain what is meant by satellite attitude and briefly describe two forms of attitude control. (08 Marks)
- c. Explain the three-axis method of satellite stabilization. (04 Marks)
- 7 a. Explain with the help of a neat block diagram of home terminal for DBS TV/FM reception. (10 Marks)
- b. Bring out the comparison between MATV and CATV systems. (10 Marks)
- 8 a. Explain in detail the operation of the spade system of demand assignment. What is the function of the common signaling channel? (08 Marks)
- b. Bring out a comparison between FDMA and TDMA with respect to their up link power requirements. (06 Marks)
- c. Explain briefly satellite-switched TDMA. (06 Marks)

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06TE64

Sixth Semester B.E. Degree Examination, June–July 2009
Transmission Lines and Antennas

Time: 3 hrs.

Max. Marks:100

- Note : 1. Answer any FIVE full questions, choosing atleast TWO questions from each part.**
2. Smith chart usage is permitted.
3. Standard notations are used.

PART - A

- 1 a. Obtain the required condition for a transmission line to have neither frequency nor delay distortion. (10 Marks)
- b. A generator of 1-0 volt, 1kHz supplies power to 100 km open wire line terminated in Z_0 and having following parameters.
 $R = 10.4$ ohms / km ; $L = 0.00367$ henry per km ; $G = 0.8 \times 10^{-6}$ mhos per km
 $C = 0.00835$ μ F per km.
 Find, characteristic impedance, Propagation constant, wavelength of the propagating wave, sending end current. (10 Marks)
- 2 a. Comment with Justification on the correctness of the statement – “Standard reference antenna for the directive gain is the isotropic antenna”. (05 Marks)
- b. Comment with justification on the correctness of the statement – “A quarter wave section of the line may be thought of as a transformer to match a load and to a source impedance”. (05 Marks)
- c. A line of characteristic impedance 200 ohms is terminated in a load of 80 – j100 ohms. Determine the location and length of the single stub matching section. The characteristic impedance of stub is same as that of line. (10 Marks)
- 3 a. Derive Frii’s Transmission formula. (10 Marks)
- b. Define Stray factor, Effective length, Effective Aperture. State the expressions, notations used. (10 Marks)
- 4 Write short notes on :
 - a. T and π equivalent to lines. (07 Marks)
 - b. Telephone cables. (07 Marks)
 - c. Antenna Field Zones. (06 Marks)

PART - B

- 5 a. Find the directivity for the source with unidirectional cosine squared power pattern. (05 Marks)
- b. Illustrate the principles of pattern multiplication. (05 Marks)
- c. Derive the expression for an N-element uniform array. Further show that the peaks of this array factor are given by the solution of the equation.
 $N \cdot \tan(\psi/2) = \tan(N\psi/2)$. (10 Marks)
- 6 a. Derive the expressions for Electric and magnetic fields of a short dipole. (10 Marks)
- b. Give the properties of Horn Antenna. (05 Marks)
- c. Illustrate – Babinet’s Principle. (05 Marks)
- 7 a. Derive instantenons electric field at a large distancce ‘r’ from a loop antenna of radius ‘a’. (10 Marks)
- b. Give the properties of Parabolic reflector. How a parabolic reflector servers the purpose of transmitting and receiving antenna? Discuss in detail its short comings. (10 Marks)
- 8 Write short notes on :
 - a. Broad side array with non uniform amplitude distribution. (07 Marks)
 - b. Lens antenna. (07 Marks)
 - c. Plasma antenna. (06 Marks)
